IN THE CLAIMS:

Please amend and re-number claims 9-20 and add claims 20-25 as follows.

1. (Original): A liquid crystal display device comprising:

a pair of substrates,

a liquid crystal layer interposed between said pair of substrates,

a wiring having a stacked structure layer formed on one of said pair of substrates,

a transparent conductive film formed over said wiring,

said wiring includes a first layer of aluminum or an alloy comprising essentially of aluminum, and at least a second layer of material selected from the group including of molybdenum, aluminum, chromium, tungsten, silver, and copper.

2. (Original): The liquid crystal display device according to claim 1 wherein said second layer is formed on said first layer.

3. (Original): The liquid crystal display device according to claim 1 wherein said transparent conductive film includes at least one of: ITO, IZO and IGO.

4. (Original): The liquid crystal display device according to claim 1 further including a plurality of pixel parts being constructed with a plurality of gate lines and a plurality of drain lines arranged in a matrix on one of said pair of substrates, and a switching element being provided in each of said pixel parts,

wherein one of said plurality of drain lines comprises said wiring.

5. (Original): The liquid crystal display device according to claim 1 further including a plurality of pixel parts being constructed with a plurality of gate lines and a plurality of drain lines arranged in a matrix on one of said pair of substrates, and a switching element being provided in each of said pixel parts,

wherein one of said plurality of gate lines comprises said wiring .

6. (Original): The liquid crystal display device according to claim 5 wherein said plurality of gate lines are formed along a first direction in one of said pair of substrates, said plurality of drain lines formed along a second direction in one of said pair of substrates, a plurality of counter voltage signal lines formed along the first direction in one of said pair of substrates,

wherein said one of plurality of counter voltage signal lines comprises said wiring.

7. (Original): The liquid crystal display device according to claim 6 further including a counter electrode disposed in said pixel part and connected with said one of plurality of counter voltage signal lines, said counter electrode having a rectilinear shape or a comb shape.

~~9~~8. (Re-numbered): The liquid crystal display device according to claim 7 further including a comb-shape pixel electrode disposed in said pixel part and connected with said switching element.

~~10~~9. (Re-numbered and Currently amended): The liquid crystal display device according to claim ~~9~~ 8 further including an insulation layer, wherein said counter electrode is formed on one of said pair of ~~electrodes~~substrates, said insulating layer is formed over said counter electrode, said pixel electrode is formed on said insulating layer.

~~11~~10. (Re-numbered and Currently amended): The liquid crystal display device according to claim ~~9~~ 8 further including a scan signal applied through one of said plurality of gate lines to said switching element, a video signal is applied through one of said plurality of drain lines and said switching element to said pixel electrode, said switching element formed proximate to a crossing point between said one of said of drain lines and said one of said gate lines.

~~12~~11. (Re-numbered and Currently amended): The liquid crystal display device according to claim ~~9~~ 8 wherein said pixel electrode has a zigzag-shaped structure.

~~13~~12. (Re-numbered and Currently amended): The liquid crystal display device according to claim ~~9~~ 8 wherein said pixel electrode has a comb-shaped structure.

~~14~~13. (Re-numbered and Currently amended): The liquid crystal display device according to claim ~~13~~ 12 further including an insulation layer and an organic layer, wherein said counter electrode is formed on one of said pair of ~~electrodes~~substrates, said insulating layer is formed over said counter electrode, said organic layer is formed over said insulating layer, said pixel electrode is formed on said organic layer.

~~15~~14. (Re-numbered): A liquid crystal display device comprising:

a pair of substrates,

a liquid crystal layer interposed between said pair of substrates,

drain lines and gate lines formed on one of said pair of substrates and crossing each other in a matrix form,

counter voltage lines formed on one of said pair of substrates and being disposed between said gate lines,

wherein at least one of said drain lines, said gate lines and said counter voltage lines includes a multi-layered structure covered with a transparent conductive film, said multi-layered structure comprising an aluminum layer or an alloy layer comprising essentially of aluminum and a high-melting point metal layer, said transparent conductive film including one of ITO, IZO and IGO.

~~16~~15. (Re-numbered and Currently amended): The liquid crystal display device according to claim ~~15~~ 14 further including a pixel electrode formed on one of said pair of substrates and having a comb-shaped structure, and a switching element formed

proximate to a crossing point between said at least one of said drain lines and said gate lines and connected with said pixel electrode.

~~17~~16. (Re-numbered and Currently amended): The liquid crystal display device according to claim ~~16~~ 15 further including a sheet of counter electrode disposed on one of said pair of substrates in opposed relation to said pixel electrode and connected with one of said counter voltage lines.

~~18~~17. (Re-numbered and Currently amended): The liquid crystal display device according to claim ~~16~~ 15 further including a comb-shaped counter electrode disposed on one of said pair of substrates in opposed relation to said pixel electrode and connected with one of said counter voltage lines.

~~19~~18. (Re-numbered and Currently amended): A liquid crystal display device comprising:

a pair of substrates,

a liquid crystal layer interposed therebetween,

a thin film transistor having a gate electrode, a source electrode and a drain electrode formed on one of said pair of substrates;

a gate line connected to said gate electrode,

a drain line connected to said drain electrode,

a pixel electrode connected to said source electrode and having an approximately a slit shape structure, a comb-shaped structure, or a zigzag-shaped structure,

a counter electrode being any of ITO, IZO or IGO and arranged in opposed relation to said pixel electrode,

a counter voltage line connected to said counter electrode,

wherein said counter voltage line comprising a triple-layered structure including an alumina first layer, a high-melting point metal second layer, and a third layer of aluminum or an alloy comprising essentially aluminum,

said high-melting point metal second layer connected to said counter electrode through an opening in said alumina first layer.

~~20~~19. (Re-numbered and Currently amended): The liquid crystal display device according to claim ~~19~~ 18 wherein said alumina first layer and said high-melting point metal second layer are formed on said third layer, and

said high-melting point metal second layer formed through said alumina layer from a surface side of a portion of said alumina layer to said third layer, and connected to said counter electrode.

20. (New): a liquid crystal display device comprising:

a pair of substrates and a liquid crystal layer interposed therebetween;

a thin film transistor having a gate electrode, a source electrode, and a drain electrode formed on one of said pair of substrates;

a gate line connected to said gate electrode;

a drain line connected to said drain electrode;

a pixel electrode connected to said source electrode and having an approximately a slit-shaped structure, a comb-shaped structure, or a zigzag-shaped structure;

a counter electrode comprising one ITO, IZO, and IGO, and arranged in opposed relation to said pixel electrode;

a counter voltage line connected to said counter electrode,

wherein said counter voltage line comprises a triple-layered structure including a molybdenum or a titanium first layer, an aluminum second layer, and a molybdenum or titanium third layer.

21. (New): The liquid crystal display device according to claim 20 wherein said first layer is connected to said counter electrode.

22. (New): The liquid crystal display device according to claim 21 wherein a width of said first layer is greater than a width of said third layer.

23. (New): A liquid crystal display device comprising:

a pair of substrates and a liquid crystal layer interposed therebetween;

a thin film transistor having a gate electrode, a source electrode and a drain electrode formed on one of said pair of substrates;

a gate line connected to said gate electrode;

a drain line connected to said drain electrode;

a pixel electrode connected to said source electrode;

a counter electrode being one of ITO, IZO, and IGO, and arranged in opposed relation to said pixel electrode;

a counter voltage line connected to said counter electrode,

wherein said counter voltage line comprises a triple-layered structure including a molybdenum or a titanium first layer, an aluminum second layer, and a molybdenum or titanium third layer,

at least one of said pixel electrode and said counter electrode having an approximately a slit-shaped structure, a comb-shaped structure, or a zigzag-shaped structure

24. (New): The liquid crystal display device according to claim 23 wherein said first layer is connected to said counter electrode.

25. (New): The liquid crystal display device according to claim 24 wherein a width of said first layer is greater than a width of said third layer.